

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Yoshiaki Mori, et al.

Group Art Unit: 1756

Serial No.: 10/026,286

Examiner: Daborah Chacko Davis

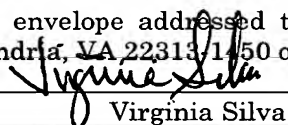
Filed: December 20, 2001

Title: Pattern Forming Method and Apparatus Used for Semiconductor Device, Electric Circuit, Display Module, and Light Emitting Device

**CERTIFICATE OF MAILING**

I hereby certify that this Response is being deposited with the United States Postal Service with sufficient postage as "First Class" mail in an envelope addressed to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this date.

Date: August 25, 2005

  
Virginia Silva

**RESPONSE B**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated May 25, 2005 (Paper No./Mail Date 0518), please amend the above-identified application as follows: